INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO. 1035-660		SERIAL NO. 10/593,695
	APPLICANT Yasuhiko NAKASHIMA		
(Use several sheets if necessary)	FILING DATE September 20, 2006		GROUP 2183

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	2008/0250232	10-9-2008	Yasuhiko NAKASHIMA (corresponds to JP 2005-284683)			
	2002/0152368	10-17-2002	Tomohiro NAKAMURA (corresponds to JP 2002-312162)			

FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
	2005-284683	10-13-2005	Japan				X
	2002-312162	10-25-2002	Japan				X

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

	Japanese Office Action w/ partial English translation dated July 13, 2010 (5 pages total).
	Yasuhiko NAKASHIMA et al., "A Speedup Technique with Function Level Value Reuse and Parallel Precomputation", Paper in Symposium on Parallel Processing, 2002, Information Processing Society of Japan, May 29, 2002, Vol. 2002, No. 8, pages 269-276 (w/ partial English translation) (13 pages total).
	Katsuya OGATA et al., "A Speedup Technique with Function Level Value Reuse and Parallel Precomputation", Research Paper for Information Processing Society of Japan, Vol. 2002, No. 22, IPSJ SIG Notes, Information Processing Society of Japan, March 8, 2002, Vol. 2002, No. 22, pages 163-168 (w/ partial English translation) (14 pages total).
	Tomoaki TSUMURA et al., "Fast Reuse Test of the Memory Values on Parallel Early Computation", Article in Journal of Information Processing Society of Japan, Computing System, Vol. 45, No. SIG1 (ACS 4), Information Processing Society of Japan, January 2004, pages 31-42 (w/ partial English translation) (15 pages total).
	Youfeng WU et al., "Better Exploration of Region-Level Value Locality with Integrated Computation Reuse and Value Prediction", Microprocessor Research Labs (MRL), Intel Corporation, Santa Clara, CA, pages 98-108; http://www.intel.com/research/mrl

*Examiner		Date Considered	

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Initial a copy of this form with next communication to applicant.

1689570